



PATENT APPLICATION
Q-51885

#20/D/C
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Akitaka KIMURA

Appln. No.: 09/161,981

Group Art Unit: 2812

Confirmation No.: 8269

Filed: September 29, 1998

Examiner: Mulpuri, S

For: SEMICONDUCTOR LAYER FORMED BY SELECTIVE DEPOSITION AND
METHOD FOR DEPOSITING SEMICONDUCTOR LAYER

PROPOSED DRAWING CORRECTION

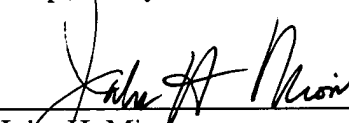
BOX AF

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Enclosed are copies of the drawing sheets containing Fig. 8 and Fig. 9 and on which it is
proposed (as indicated in red ink) to delete the legend "Prior Art" from each of Figs. 8 and 9.

Respectfully submitted,



John H. Mion
Registration No. 18,879

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WASHINGTON OFFICE



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PATENT TRADEMARK OFFICE
Date: March 20, 2003

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